

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:)	
GENDRIER ET AL.)	Examiner: Thong Q. LE
)	
Serial No. 10/511,712)	Art Unit: 2827
)	
Confirmation No. 6747)	
)	
Filing Date: OCTOBER 15, 2004)	
)	
For: ELECTRICALLY ERASABLE AND)	
PROGRAMMABLE, NON-VOLATILE)	
SEMICONDUCTOR MEMORY DEVICE)	
HAVING A SINGLE LAYER OF GATE)	
MATERIAL, AND CORRESPONDING)	
MEMORY PLANE)	

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action of August 21, 2006,
please enter the amendments and consider the remarks set out
below.

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this filing.

Remarks begin on page 23 of this filing.